	Subclass	
	Class	ISSUE CLASSIFICATION

JC874 S. PTO
09/612067
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5C/
5C825

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED Eu(S) Q.A. CC	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/612067		370	257	2664 2661	KADING

APPLICANTS Joel Naumann

TITLE Method and apparatus for communication of missing logic in a PCI based system by enabling or disabling PCI config cycles

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)							
CLASS	SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION										

☐ Continued on Issue Slip Inside File Jacket

☐ **TERMINAL DISCLAIMER**	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				**NOTICE OF ALLOWANCE MAILED**	
	(Assistant Examiner)		(Date)		
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____					
				ISSUE FEE	
				Amount Due	Date Paid
	(Primary Examiner)		(Date)		
☐ The terminal ____months of this patent have been disclaimed.				**ISSUE BATCH NUMBER**	
	(Legal Instruments Examiner)		(Date)		

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